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The reduction in transistor size allows more and more transistors to be packed into devices like central processing units (CPUs) in computers. The overall trend of shrinking component size leading to

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doubling the number you can fit on a device is known as Moore's Law.

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Achieve accurate and reliable parameter extraction using this complete survey of state-of-the-art techniques and methods. A team of experts from industry and academia provides you with insights into a range of key topics, including parasitics, intrinsic extraction, statistics, extraction uncertainty, nonlinear and DC parameters, self-heating and traps, noise, and package effects. Learn how similar approaches to parameter extraction can be applied to different technologies. A variety of real-world industrial examples and measurement results show you how the theories and methods presented can be used in practice. Whether you use transistor models for evaluation of device processing and you need to understand the methods behind the models you use, or you want to develop models for existing and new device types, this is your complete guide to parameter extraction.

Server+ is one of the newest certifications from CompTIA, the sponsor of such vendor-neutral IT certifications as A+ and Network+. Server+ is positioned alongside Network+ as a follow-up to A+ certification. The Server+ exam focuses on network hardware while the Network+ exam focuses on network software. The Server+ exam certifies the knowledge of mid- to upper-level technicians with 18-24 months of strong IT experience using hardware functionality, including server installation, troubleshooting, support, and second level support.

An up-to-date, practical guide on upgrading from silicon to GaN, and how to use GaN transistors in power conversion systems design This updated, third edition of a popular book on GaN transistors for efficient power conversion has been substantially expanded to keep students and practicing power conversion engineers ahead of the learning curve in GaN technology advancements. Acknowledging that GaN transistors are not one-to-one replacements for the current MOSFET technology, this book serves as a practical guide for understanding basic GaN transistor construction, characteristics, and applications. Included are discussions on the fundamental physics of these power semiconductors, layout, and other circuit design considerations, as well as specific application examples demonstrating design techniques when employing GaN devices. GaN Transistors for Efficient Power Conversion, 3rd Edition brings key updates to the chapters of Driving GaN Transistors; Modeling, Simulation, and Measurement of GaN Transistors; DC-DC Power Conversion; Envelope Tracking; and Highly Resonant Wireless Energy Transfer. It also offers new chapters on Thermal Management, Multilevel Converters, and Lidar, and revises many others throughout. Written by leaders in the power semiconductor field and industry pioneers in GaN power transistor technology and applications Updated with 35% new material, including three new chapters on Thermal Management, Multilevel Converters, Wireless Power, and Lidar Features practical guidance on formulating specific circuit designs when constructing power conversion systems using GaN transistors A valuable resource for professional engineers, systems designers, and electrical engineering students who need to fully understand the state-of-the-art GaN Transistors for Efficient Power Conversion, 3rd Edition is an essential learning tool and reference guide that enables power conversion engineers to design energy-efficient, smaller, and more cost-effective products using GaN transistors.

This newly revised edition adds two entirely new chapters, one of LDMOS high power RF transistors and how they differ from bipolars, and TMOS FETs, etc. as well as another chapter on designing high power RF amplifiers using LDMOS.

All model parameters are fundamentally coupled together, so that directly measured individual parameters, although widely used and accepted, may initially only serve as good estimates. This comprehensive resource presents all aspects concerning the modeling of semiconductor field-effect device parameters based on gallium-arsenide (GaAs) and gallium nitride (GaN) technology. Metal-semiconductor field-effect transistors (MESFETs), high electron mobility transistors (HEMTs) and heterojunction bipolar transistors (HBTs), their structures and functions, and existing transistor models are also classified. The Shockley model is presented in order to give insight into semiconductor field-effect transistor (FET) device physics and explain the relationship between geometric and material parameters and device performance. Extraction of trapping and thermal time constants is discussed. A special section is devoted to standard nonlinear FET models applied to large-signal measurements, including static-/pulsed-DC and single-/two-tone stimulation. High power measurement setups for signal waveform measurement, wideband source-/load-pull measurement (including envelope source-/load pull) are also included, along with high-power intermodulation distortion (IMD) measurement setup (including envelope load-pull). Written by a world-renowned expert in the field, this book is the first to cover of all aspects of semiconductor FET device modeling in a single volume.

Section-I: Solid State Physics | Section-Ii Electronics | Section-Iii: Nuclear And Particle Physics

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